

# SMPS Evaluation Board with CoolSET™ F3 ICE3B0565

Power Management & Supply



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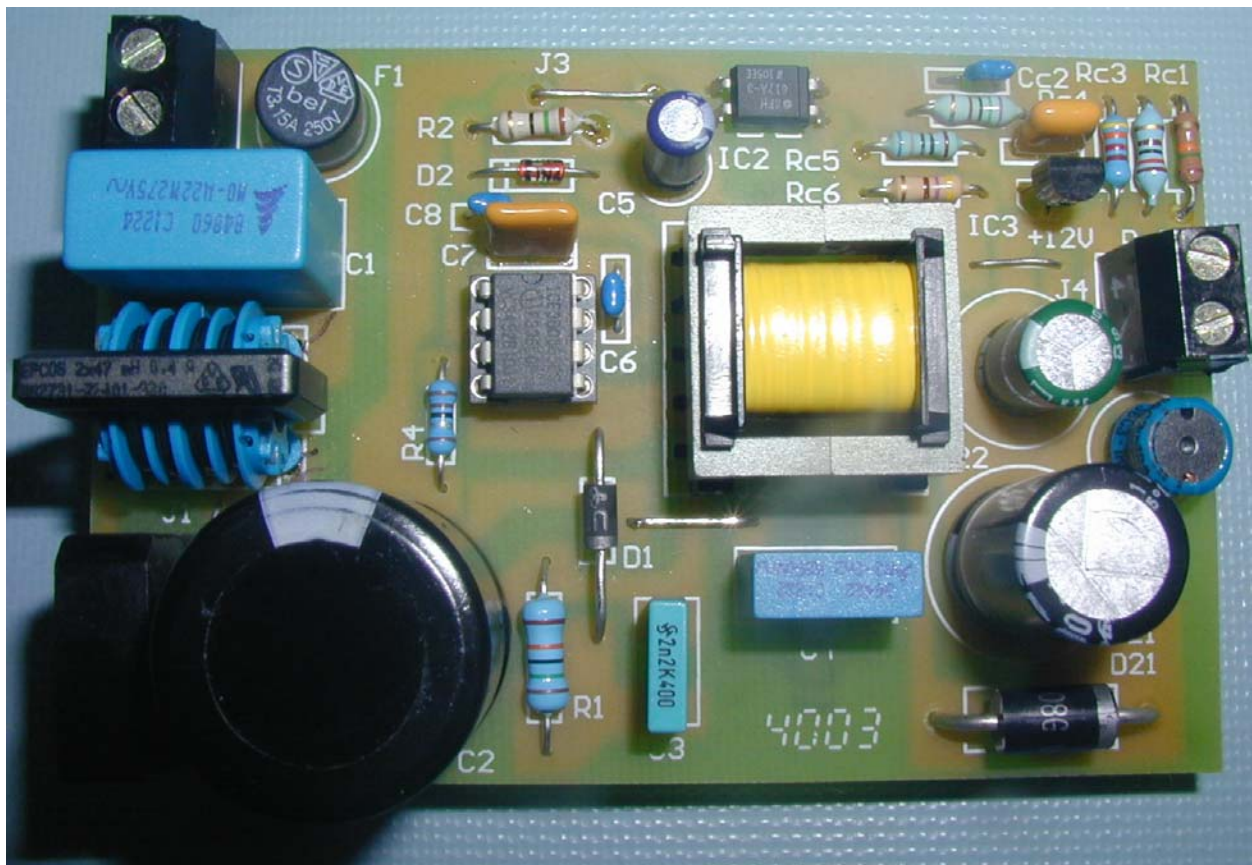
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## Abstract

This application note is a description for a switching mode power supply Evaluation Board designed in a typical off-line flyback converter topology that utilizes the ICE3B0565 F3 CoolSET™. The application operates in discontinuous conduction mode and is especially suitable as an AC/DC power supply for LCD monitors, adapters for printers and notebook computers, DVD players and set-top boxes in addition to industrial auxiliary power supplies. Designed based on the proven technology of F2 CoolSET™ ICE2xxxx, the F3 CoolSET™ provides Active Burst Mode to reach the lowest Standby Power Requirement at less than 100mW during no load conditions. In Active Burst Mode the ripple of the output can also be reduced to less than 1%. Furthermore, an adjustable Blanking Window by varying the value of Soft Start capacitor is being offered to support applications which needs surge power for a limited period of time (e.g. switching on capacitive loads).

## 1 Evaluation Board



## 2 List of Features

<b>Features Of CoolSET™ F3 ICE3B0565</b>	
650V avalanche rugged CoolMOS™ with built in switchable Startup Cell	
Active Burst Mode for lowest Standby Power @ light load controlled by Feedback signal	
Fast load jump response in Active Burst Mode	
67kHz fixed switching frequency	
Auto Restart Mode for Overtemperature Detection, Overvoltage Detection, Overload, Open Loop and VCC Undervoltage	
Blanking Window for short duration high current	
User defined Soft Start	
Max Duty Cycle 72%	
Internal Leading Edge Blanking	
Soft switching for low EMI	

## 3 Technical Specifications

Input voltage	85VAC~265VAC
Input frequency	50Hz, 60Hz
Output voltage and current	12VDC, 1.25A
Output power	15W
Efficiency	>80% at full load

## 4 Circuit Description

### 4.1 Line Input

The AC line input side comprises the input fuse F1 as over-current protection. The choke L1, X2-capacitors C1 and Y2-capacitor C4 act as radio interference suppressors. After the bridge rectifier BR1 and the input capacitor C2, a voltage of 80 to 380 VDC depending on input voltage is available.

## **4.2 PWM Control and Power Stage**

The PWM pulse is generated by 8-pin CoolSET™ F3 ICE3B0565. ICE3B0565 is an integrated power IC which includes both of the current-mode PWM controller and a CoolMOS™ with 650V breakdown voltage. The control IC and CoolMOS™ are fabricated by the different optimized chip technologies respectively and no compromise like monolithic approaches is necessary.

## **4.3 Clamping Network**

R1, C3 and D1 dissipate the energy of the leakage inductance.

## **4.4 Primary Current Sense**

The primary current is sensed by the external shunt resistor R4. The sense voltage is fed into ICE3B0565 and a cycle by cycle current limiting is achieved. Primary current is being converted to a corresponding voltage level at CS pin. A 220ns leading edge blanking is provided to avoid leading edge spikes from distorting the current limiting.

## **4.5 Output Stage**

On the secondary side the power is coupled out by an ultra-fast diode UF5401. The capacitor C21, provide energy buffering following with the LC filter, L21 and C22 to reduce the output voltage ripple considerably. Storage capacitor C21 is selected to have an internal resistance as small as possible (ESR) to minimizes the output voltage ripple

## **4.6 Feedback Loop**

The output voltage is sensed by the voltage divider of Rc1, Rc2 and Rc3 and compare to TL431 internal reference voltage. The output voltage of TL431, IC3 is transferred to the primary via an optocoupler, IC2 for regulation control. The secondary regulation control is adopted with TL431 and optocoupler. Cc1, Cc2, Rc4 being the compensation network constitutes the external circuitry of the error amplifier of TL431. This circuitry allows the feedback to be precisely matched to dynamically



varying load conditions, thereby providing stable control. The maximum current through the optocoupler diode and the voltage reference is limited by the resistor Rc6 and Rc5. Optocoupler IC2 is used for floating transmission of the control signal to the “FB” input of the ICE3B0565.

#### **4.7 Blanking Window for Load Jump / Active Burst Mode**

In case of Load Jumps the Controller provides a Blanking Window before activating the Overvoltage Protection and entering the Auto Restart Mode. This time is generated by charging up the Soft Start capacitor from 4.4V to 5.4V. Within this timeframe the voltage at Feedback pin can rise up above 4.8V, without switching off due to Overload Protection. During this operation the transferred power is limited to the maximum peak current defined by the value of the sense resistor. The same procedure happens to the external Soft Start capacitor if a low load condition is detected when  $V_{FB}$  is falling below 1.32V. Only after  $V_{SOFTS}$  has exceeded 5.4V and  $V_{fb}$  is still below 1.32V, Active Burst Mode is entered.

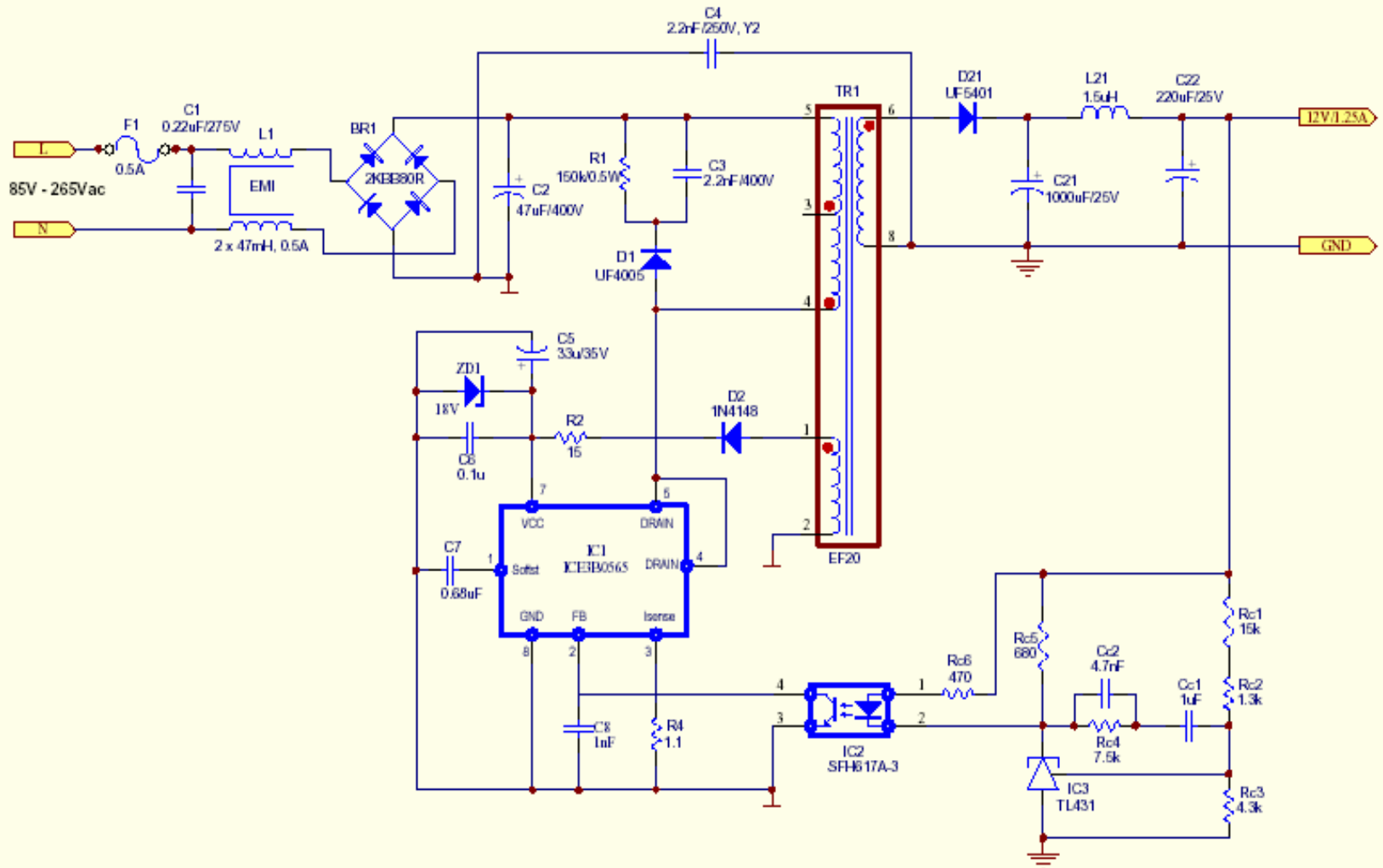
#### **4.8 Active Burst Mode**

At light load condition, the SMPS enters into Active Burst Mode. The controller is always active at this state.  $V_{CC}$  must therefore be above the switch off threshold  $V_{CCoff} = 8.5V$ . While supporting low ripple on  $V_{OUT}$  and fast response on load jump, efficiency also increased significantly during Active Burst Mode. When the voltage level at FB falls below 1.32V, capacitor  $C_7$  at SOFTST pin is allowed to charge starting from the clamped voltage level at 4.4V in Normal Operating Mode. Active Burst Mode is entered if  $V_{SOFTST}$  exceeds 5.4V. A Blanking Window as mentioned earlier which can be adjusted by manipulating  $C_7$  is generated to avoid a sudden entering of Burst Mode due to load jump.

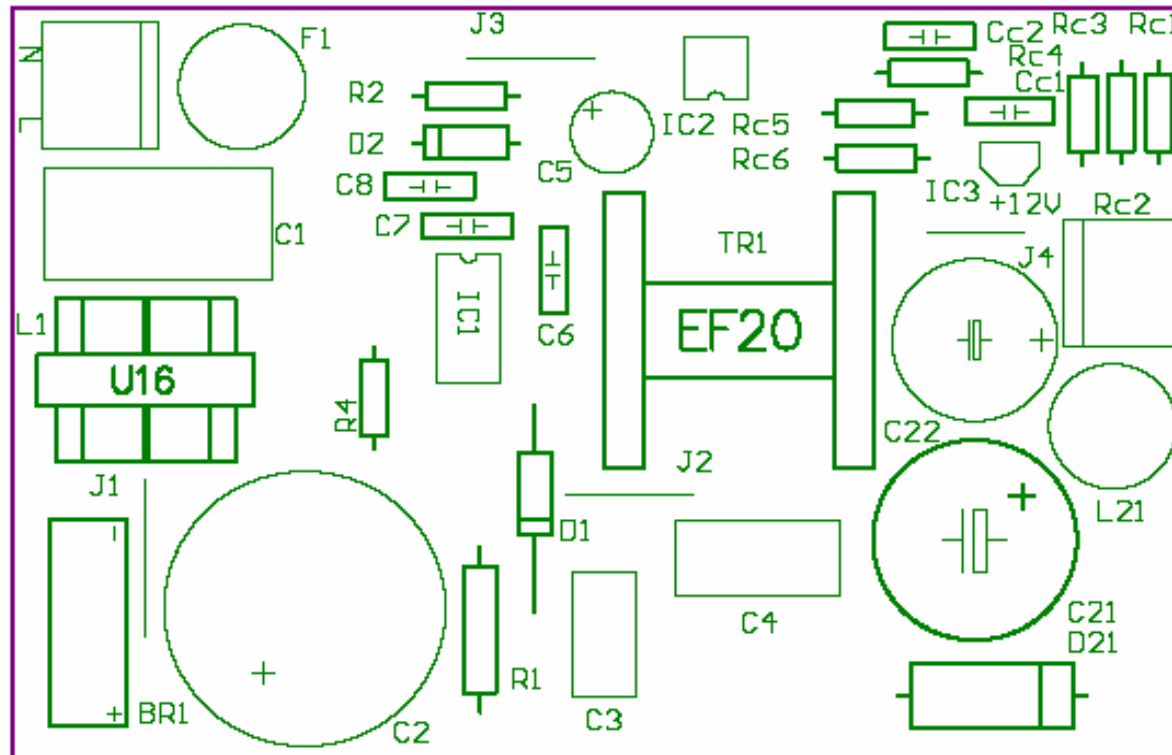
During Active Burst Mode the current sense voltage limit at  $I_{SENSE}$  pin,  $V_{ISENSE}$ , is set to 0.257V to reduce the conduction losses. All the internal circuits are switched off except the reference and bias voltages to reduce the total  $V_{CC}$  current consumption to below 1.1mA. The FB voltage is changing like a sawtooth between 3.4 and 4V. To leave Burst Mode, FB voltage must exceed 4.8V. This resets the Active Burst Mode and turns the SMPS into Normal Operating Mode. Maximum current can now be provided to stabilize  $V_{OUT}$ .



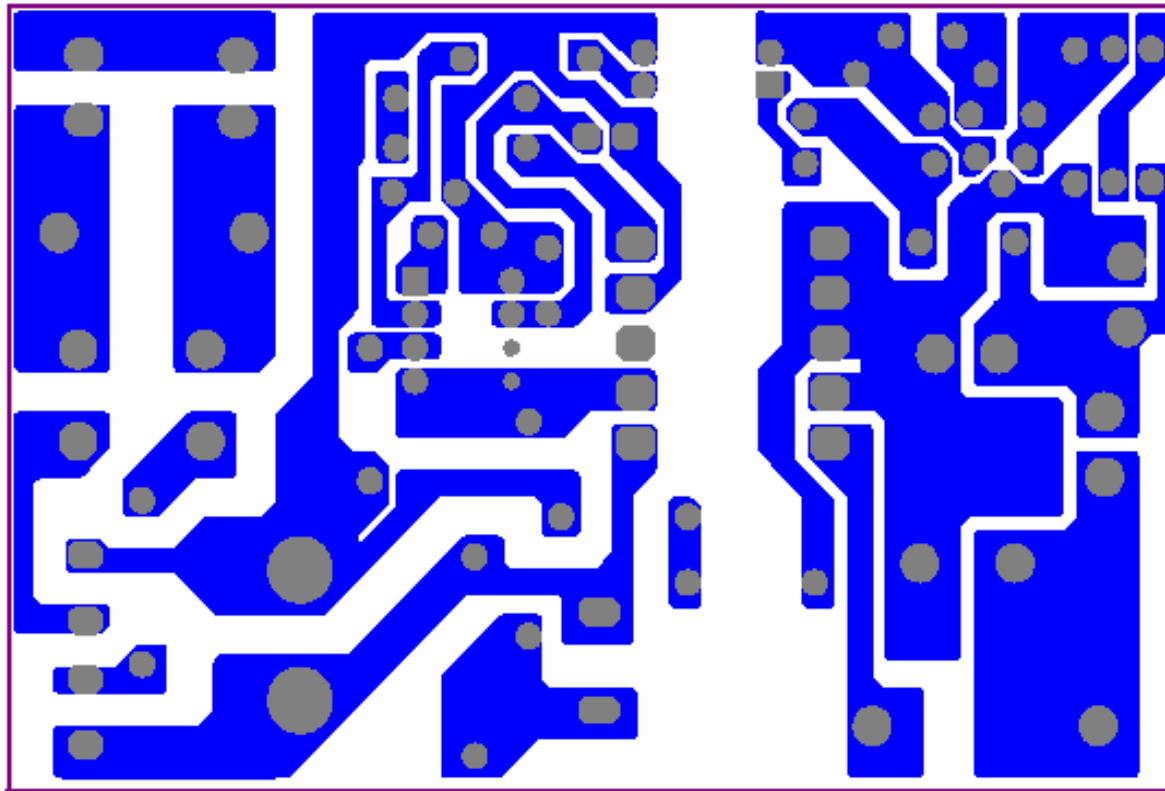
## 5 Circuit Diagram



## 6 PCB Top Layer



## 7 PCB Bottom Layer



## 8 Component List

Items	Part	Type	Quantity
1	BR1	2KBB80R	1
2	C1	0.22uF/275V, X2 Capacitor	1
3	C2	47uF/400V	1
4	C3	2.2nF/400V	1
5	C4	2.2nF/250V, Y2 Capacitor	1
6	C5	33uF/35V	1
7	C6	100nF/50V	1
8	C7	0.68uF/50V	1
9	C8	1nF/50V	1
10	C21	1000uF/25V	1
11	C22	220uF/25V	1
12	Cc1	1uF/50V	1
13	Cc2	4.7nF/50V	1
14	D1	UF4005	1
15	D2	1N4148	1
16	D21	UF5401	1
17	F1	3.15A/250V	1
18	IC1	ICE3B0565	1
19	IC2	SFH617A-3	1
20	IC3	TL431CLP	1
21	L1	2 x 68mH, 0.4A	1
22	L21	1.2uF	1
23	R1	150K , 0.5W , 1%	1
24	R2	15 , 0.25W, 5%	1
25	R4	1.1 , 0.25W , 5%	1
26	Rc1	15K , 0.5W , 1%	1
27	Rc2	1.3k , 0.25W , 5%	1
28	Rc3	4.3K, 0.25W , 1%	1
29	Rc4	7.5K , 0.25W , 5%	1
30	Rc5	680, 0.25W, 5%	1
31	Rc6	470, 0.25W , 5%	1

32	TR1	EF20 N67	1
33		Bobbin EF20	
34		Connector 2pole.	2
35	ZD1	18V	1
36		25mm Jumper	3

## 9 Transformer Layout

Start	Stop	No of turns	Wire Size	Layer
1	2	11	3X0.2mm	Aux
3	5	33	1X0.2mm	Primary
6	8	10	1X0.5mm	Secondary
4	3	33	1X0.2mm	Primary
Core Inside				

Bobbin: Horizontal Version

Core: EF20/N67

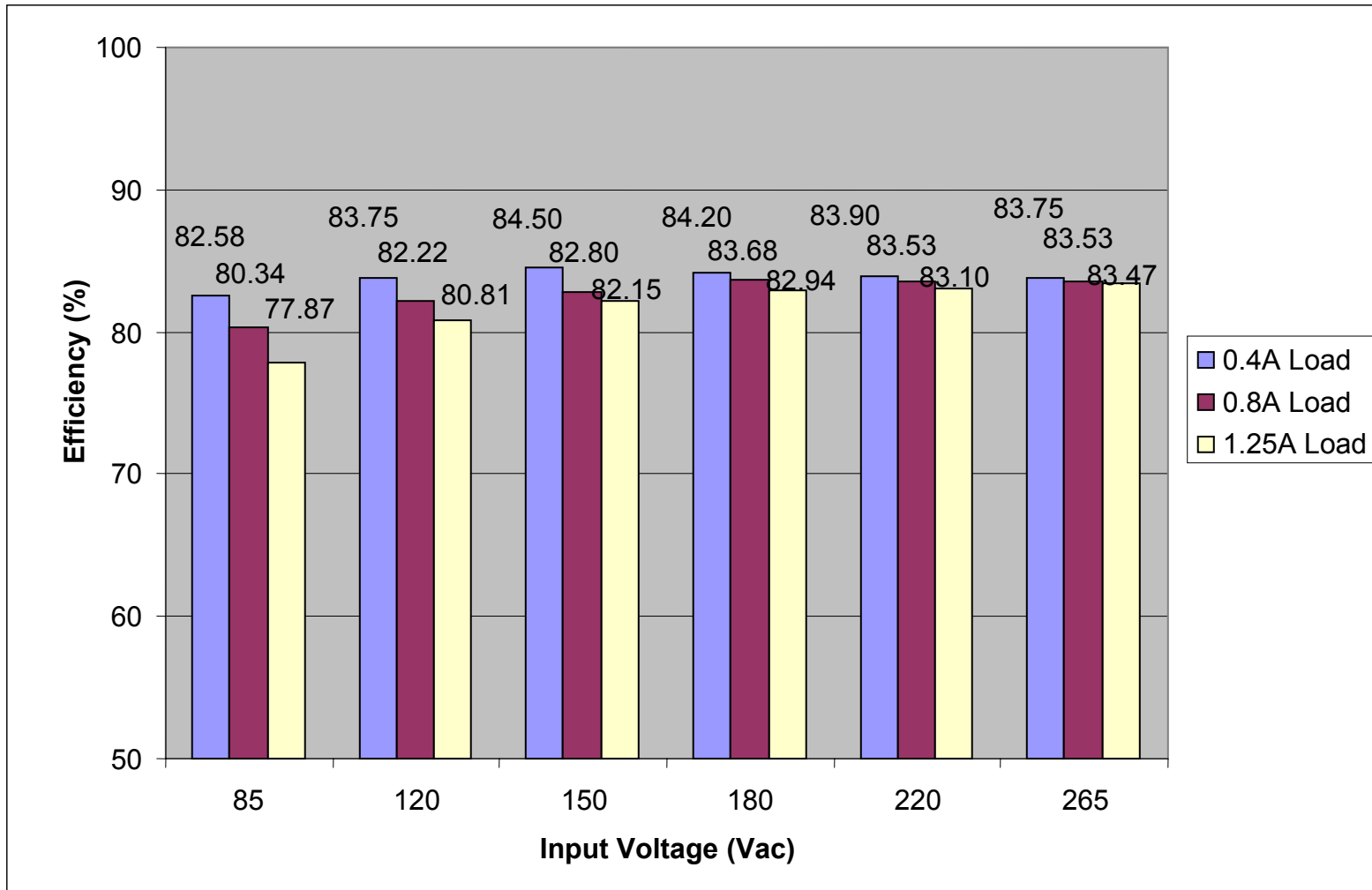
Primary Inductance,  $L_p=695\mu\text{H}$ , measured between pin 4 and pin 5  
(Gapped to Inductance)

## 10 Test Results

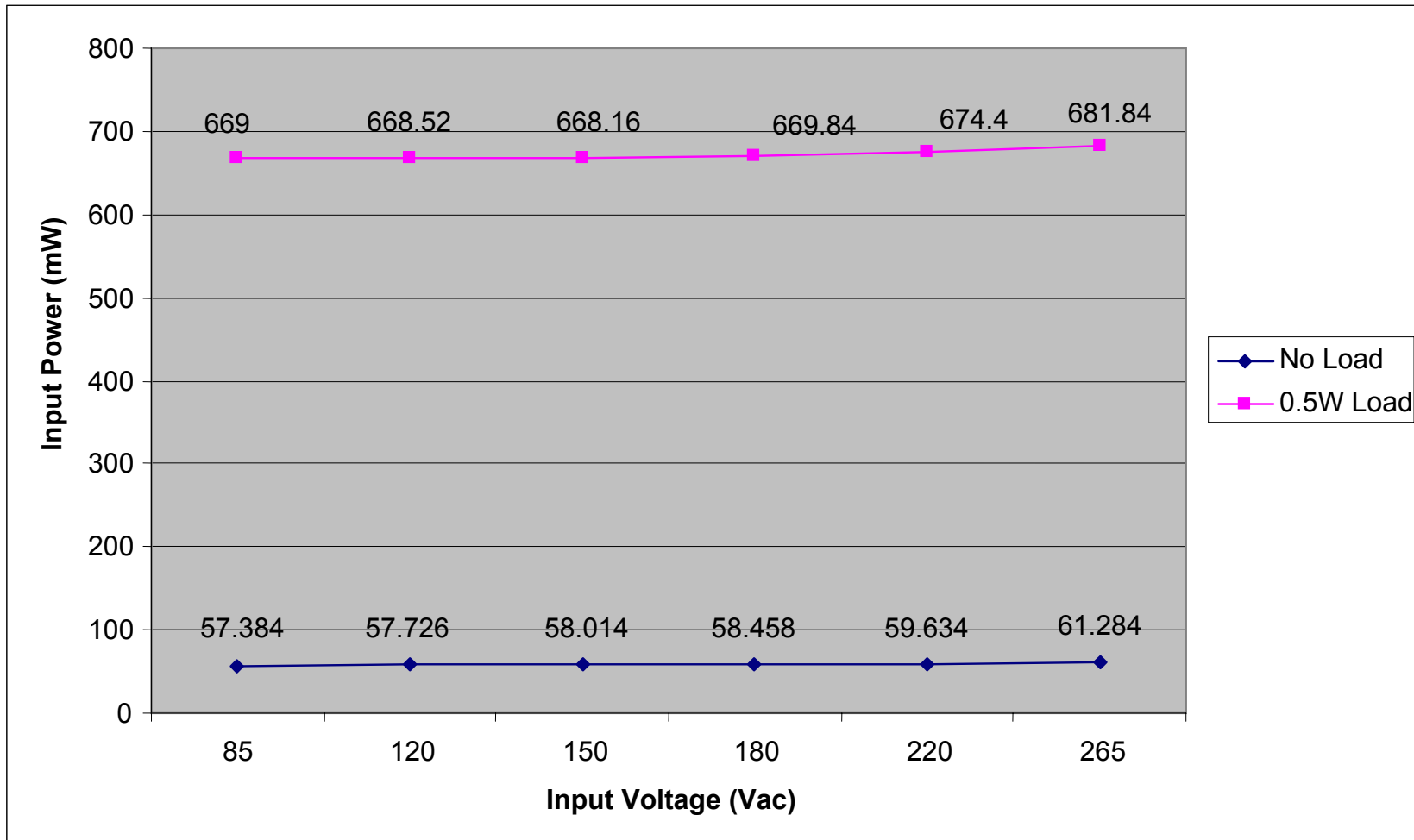
### 10.1 Load test (table and figure)

Vin (Vac)	Iin (A)	Pin (W)	Vo (Vdc)	Io (A)	Po (W)	Efficiency (%)
85	0.137	5.73	11.83	0.4	4.732	82.58
	0.246	11.78	11.83	0.8	9.464	80.34
	0.367	18.99	11.83	1.25	14.7875	77.87
120	0.189	5.65	11.83	0.4	4.732	83.75
	0.193	11.51	11.83	0.8	9.464	82.22
	0.281	18.3	11.83	1.25	14.7875	80.81
150	0.095	5.6	11.83	0.4	4.732	84.50
	0.1675	11.43	11.83	0.8	9.464	82.80
	0.242	18	11.83	1.25	14.7875	82.15
180	0.085	5.62	11.83	0.4	4.732	84.20
	0.149	11.31	11.83	0.8	9.464	83.68
	0.214	17.83	11.83	1.25	14.7875	82.94
220	0.075	5.64	11.83	0.4	4.732	83.90
	0.1306	11.33	11.83	0.8	9.464	83.53
	0.1878	17.78	11.82	1.25	14.775	83.10
265	0.068	5.65	11.83	0.4	4.732	83.75
	0.116	11.33	11.83	0.8	9.464	83.53
	0.167	17.7	11.82	1.25	14.775	83.47





### 10.2 Standby Power Test (Figure)



## 11 References

- [1] **Infineon Technologies**, Datasheet  
CoolSET™ - F3  
OFF-Line SMPS Current Mode Controller with integrated 650V  
Startup Cell/CoolMOS™
  
- [2] **Infineon Technologies**, Application Note  
AN-SMPS-ICE2AXXX-1  
CoolSET™  
ICE2AxXXX for OFF-Line Switch Mode Power Supply (SMPS)
  
- [3] **Infineon Technologies**, Application Note  
AN-SMPS-ICE3DS01-1  
CoolSET™  
ICE3DS01 Current Mode Controller for OFF-Line Switch Mode  
Power Supply (SMPS)
  
- [3] **APEB Power Management Chapter September**, Article  
60W SMPS design achieving <100mW standby power